

Papers Selected for International Conference on VLSI Design and Communication Systems 2010--List-I

1	CO8	THE EFFECT OF MUTUAL COUPLING IN A LINEAR ARRAY AND PLANAR ARRAY ENVIRONMENT OF A 3-ELEMENT YAGI-UDA ANTENNA	Nandini M. Ammanagi Dr. K. P. Ray	VESIT, Chembur, Mumbai SAMEER, IIT Campus, Powai, Mumbai
2	IP5	MULTI-HOP PATH TRACING OF MOBILE ROBOT WITH MULTIRANGE IMAGE	RamakantaChoudhury Chandrakanta Samal Dr Umakanta Choudhury	M..A.I.T,Delhi Acharya Narendra Dev College,Delhi Ideal college ,Gagiabad(U.P).
3	IP6	DEVELOPMENT OF IMAGE ENHANCEMENT AND IMAGE COMPRESSION ALGORITHMS FOR COLOR-IMAGES IN AN IMAGE SENSOR.	Papri Ghosh	National Institute of Science & Technology, Berhampur, Orissa
4	CO3	Grudger Nodes towards Routing security for Mobile Ad-hoc Networks	C. Chandrasekhar E V Narayana	JNT University, Anantapur.
5	CO14	DDFS IMPLENTATION USING CORDIC ALGORITHM FOR SDR TRANSMITTER	T.Karpagambigai S.Dhivyathangam	THIAGARAJAR COLLEGE OF ENGINEERING,MADURAI
6	CO16	DESIGN OF ROOT RAISED COSINE FILTER FOR 3G-WCDMA APPLICATION	Shreya Kadambi Varsha V P V Rao Sneha K S Vidya Vasudevan	RNSIT, Bangalore RNSIT, Bangalore RNSIT,Bangalore&Dr.MGR Univ., RNSIT, Bangalore, RNSIT, Bangalore
7	CO21	Superior Capabilities of UWB Impulse Beamforming over Narrowband Beamforming	Sunil Bojanapally RaksheshS.Kshetrimayum	Indian Institute of Technology, Guwahati, Assam,India
8	CO24	Scheduling and Route Optimization in Next Generation All-IP Wireless Network	Sulata Mitra	Bengal Engineering and Science University,India
9	SP4	Performance Comparison of ROLD Statistic Based Nonlinear Filter for image Denoising	H. Gangadhar Dr. E. Srinivasan	Pondicherry Engineering College, Puducherry - 605014
10	VL14	The QV Comparator in Flash Analog to Digital Converter Design	Mrs. Meghana Kulkarni Dr. V. Sridhar Dr. G.H.Kulkarni	Gogte Institute of Tech., Bgm P.E.S. College of Engg., Mandya Gogte Institute of Tech., Bgm
11	VL15	Design of Mixed mode Fuzzy Controllers Architecture for Integrated MEMS systems: A design approach	K.K.Jajulwar A.Y.Deshmukh	G.H.Raisoni College of Engg., Nagpur.(M.S.),India

12	VL16	An Overview of DGMOSFET Structures and Applications	Prakash V. Baviskar Sanjeev N. Jain Prasad D. Vinchurkar Asif M. Shaikh	S.S.V.P.S's B. S. Deore College of Engineering, Dhule, Maharashtra, India
13	VL26	An Optimized Methodology for Designing High Speed Flash ADC	Kapil Chaudhary B.K.Kaushik Kirat Pal	Govind Ballabh Pant Engg. College, Pauri Garhwal , India I.I.T Roorkee, India
14	VL27	A Power Reduction Technique For Built-In Self Testing Using Modified Linear Feed Back Shift Register	Mayank Shakya SoundraPandian. K. K	Indian Institute of Information Technology Design and Mfg. Jabalpur, Madhya Pradesh, India
15	VL40	VLSI Implementation of Superscalar pipelining structures of a 32 bit Digital Signal Processor	Arathy Menon Aswathy Menon Sithara K Veena E Ramesh B	Amrita Vishwa Vidyapeetham Amritapuri, Clappana P.O., Kollam Dt. Kerala-690525, India
16	VL42	Design and Implementation of a Reconfigurable Baseband Modulator for SDR Architecture	Indranil Hatai Indrajit Chakrabarti	Indian Institute of Technology Kharagpur, India
17	VL44	FPGA IMPLEMENTATION OF DATA ACQUISITION MODULE USING IEEE 1394 (FIREWIRE) INTERFACE	K.Babulu K.Soundara Rajan B.V.S.Shravani	JNTU, Kakinada JNTU, Anantapur JNTU, Kakinada
18	VL4	Static Power Minimization in CMOS and Pseudo-NMOS Logic Circuits using Sleep Transistors	M. Janaki Rani J.Raja	Dr. M. G. R University, Chennai SSN College of Engineering, Chennai
19	VL5	Low-Power Pipelined MIPS Processor Design	Gautham P Parthasarathy R Karthi B	Amrita School of Engineering Amrita Vishwavidyapeetham, India
20	VL12	DESIGN OF HIGH-SPEED FLASH ADC USING CMOS LATCHED COMPARATOR WITH CURRENT STEERING LOGIC SR LATCH	A.Karthikeyan V.Srividhya	School of Electrical Sciences, Vellore Institute of Technology Vellore -632014, Tamil Nadu, India
21	VL46	Design and analysis of Floating point and Galois field multipliers using Wave-pipelining	M. Anbuselvi P. Saravanan S. Salivahanan	SSN College of Engineering Chennai, India
22	VL10	AN IEEE 802.11n ASIC implementation	Haeyoung Rha Eunyoung Choi Sok-kyu Lee	ETRI, Republic of Korea
23	VL8	Sizing Techniques for Improving Soft Error Immunity in Digital Circuits	Warin Sootkaneung Kewal K. Saluja	University of Wisconsin-Madison Madison, WI 53706, USA

24	VL11	Initial Radius Selection of Sphere Decoder for Practical Applications of MIMO Channels	F. Eshagh Hosseini A. Salahi S. Shirvani Moghaddam	Tehran 14399, Iran
25	CO15	Tracking a Mobile Target Using Bright Square in Wireless Sensor Network	A. Manaviat M. Fathy M. Nikzad	Islamic Azad University University of Science and Technology Science and Research Islamic Azad University, Tehran, I.R. Iran
26	IP2	Reference Point and Orientation Detection of Fingerprints	Shahram Mohammadi Ali Farajzadeh	University of Zanjan, Zanjan, Iran
27	SP1	Automatic Stress Detection from Speech by Using Support Vector Machines and Discrete Wavelet Transforms	Firoz Shah.A Raji Sukumar.A Babu Anto.P	School of Information Science and Technology, Kannur University, Kerala, India
28	VL6	Design of Bi-Quad filter using High CMRR, High Slew Rate, Low THD CMOS Operational Transconductance Amplifier for HF Applications	Deepa Yagain	PES Institute of Technology, Bangalore-85, India
29	VL13	Implementation of Ultra Low-Power Sequential Circuit by a Quasi-Static Single Phase Adiabatic Dynamic Logic (SPADL)	M.Chanda A.Dandapat H. Rahaman	Meghnad Saha Institute of Technology Jadavpur University, Engineering & Science University Kolkata, India
30	CO20	Impact of Multiple UWB Devices on IEEE 802.11a WLAN Systems	Santosh Reddy M. RakeshS.Kshetrimayum	Indian Institute of Technology, Guwahati, Assam, India
31	CO22	Design of RF CMOS Low Noise Amplifier for IEEE 802.11b Wireless LAN Applications	Mahesh Mudavath Manjit Kaur Gurmohan Singh	C-DAC Mohali, India
32	CO23	Security Issue of a Route Selection Algorithm in Multihomed Mobile Networks	Sulata Mitra	Bengal Engineering and Science University, India
33	VL48	A 60 Ghz Analog Phase Shifter in 65nm Bulk CMOS Process	ETB.Samuel Jigme Harrison Zhang Yue Ping	Nanyang Technological University, Singapore
34	CO26	Seamless Handover between WIMAX and GPRS	Mushtaq Md. Arman Khan Mohd Faizal Bin Ismail Kaharudin Dimiyati	University of Malaya Kuala Lumpur-50603, Malaysia