

1	IP7	Content Based Medical Image Retrieval by Combining Rotation Invariant Contourlet Features and	Arun K.S Hema P Menon	Research Sholar Assistant Professor	Amrita School of Engineering Ettimadai, Coimbatore.
2	IP9	A Robust Digital Image Watermarking Scheme using Human Visual System	K.Veeraswamy B.ChandraMohan Y.V.Bhaskar Reddy V.Radhika	Professor and HOD, ECE Dept. Professor and HOD ,ECE Dept. Associate Professor, ECE Dept. Assistant Professor, ECE Dept.	QISCET, Ongole, A.P, India., BEC, Bapatla, A.P, India. QISCET, Ongole, A.P, India. KIET, Kakinada,A.P, India.
3	VL20	Study and Implementation of Mux based FPGA in QCA technology	E.N.Ganesh V.Krishnan	Professor Student	Rajalakshmi Engineering College, Tamil Nadu, India
4	VL22	Multi-valued logic addition and multiplication in Galois field	VASUNDARA PATEL K S DR K S GURUMURTHY		
5	VL23	Low-Power and High-Performance 1-Bit CMOS Full-Adder Cell	Manikandan Raju D.N.Keerthi Kumar A.Kumaravel		Meerut Institute Of Technolgy, Meerut Paavai Engg. College, Rasipuram Paavai Engg. College, Rasipuram
6	PR4	Offline Assertion-Based Verification using VCD	AnsumanBanerjee Ujjwal Jain Kausik Datta	Interra Systems Comp. Science Dept. Interra Systems	India Pvt. Ltd Jadavpur University India Pvt. Ltd

7	IP11	Application of Optical Image Processing Method for Detecting Abnormalities of Diseased Human Organ	A. Brintha Therese S. Sundaravadivelu	School of Electrical Sciences Department of ECE	VIT University, Vellore SSN College of Engineering, Chennai
8	PR2	SEA STATE MONITORING HF RADAR CONTROLLER using LabVIEW PLATFORM	Shubhada R.Gadgil Dharmesh Verma, Dr. Meena S. Panse Dr. Kushal Tuckley	MTech Electronics Scientist, SPNE Divison,SAMEER. Professor,EEE Dept. Scientist, Head, SPNE Divison, SAMEER.	VJTI, Matunga Rd, Mumbai ,India. IIT Bombay, Mumbai, India. VJTI, Mumbai, India. IIT Bombay, Mumbai, India.
9	VL34	A 3.1 – 6GHz 0.2dB Noise Figure Common Source Low Noise Amplifier	L Akshaya Prashanthi1 V.V.Kavita.P2 V.Vaithianathan3	1,2Pre Final Students 3Assistant Professor	Department of ECE, SSN College of Engineering, Kalvakkam, Tamilnadu
10	CO28	Media-engine for Multiparty Conferencing in VoIP	Senthil Kumar Mani Bala Manikya Prasad.P Kamesh.M		HelloSoft India Ltd., Hyderabad, India.
11	CO12	Determining an Optimum Window Size in a Lined Up Manet	Chandra Kanta Samal	School of Computer and System Science	Jawaharlal Nehru University, New Delhi

12	CO29	A Dynamic Fuzzy Based Priority Scheduler for Mobile WiMAX	D.David Neels Pon Kumar ¹ K.Murugesan ² S Raghavan ³	Department of ECE Principal Department of ECE	¹ J.S.Engineering College,Tirunelveli, India ² Mahakavi Bharathiar Engineering College,Chinna Salem, India ³ National Institute of Technology, Tiruchirapalli, India
13	VL18	Performance Analysis of Low leakage 1-bit Nano-CMOS based Full Adder Cells for Mobile Applications	M.V.D.L.Varaprasad Rohit Bapna Manisha Pattanaik		ABV-Indian Institute of Information Technology and Management. Morena Link road, Gwalior-474010, Madhya pradesh, INDIA
14	VL24	Novel Circuit Technique for Reduction of Active Drain Current in Low Leakage Digital VLSI Circuits	Vaibhav Neema Shailesh Singh Chouhan Sanjiv Tokekar	Department of Electronics & Telecommunication	IET-Devi Ahilya University, Indore, India
15	VL37	Design of a 2V 1uA CMOS opamp using Adaptive Biasing Technique	Ratul Kr. Baruah	Assistant Professor, Department of ECE	Tezpur University, Assam, India

16	VL50	DR3 Memory Controller Interfa	Vikky Lakhmani Nusrat Ali Dr. Vijay Shankar Tripathi	Department of E.E.E Project Manager (VLSI Division), Department of Electronics & Communication	Uttar Pradesh Technical University, Lucknow (Uttarpradesh), INDIA HCL Technologies, Noida Motilal Nehru National Institute of Technology, (MNIT), Allahabad (U.P), INDIA
17	VL51	8-Bit Sub Threshold Ripple Carry Adders in 32nm CMOS Technology for Wireless Sensor nodes	R. SAVARI RANI DR.C.CHRISTOBERA SIR RAJAN V.HARINI	HOD/CSE Asst.Prof/ EEE II year, ECE	PRIST University, Trichy PEC, Pondicherry Saranthan College of Engg., Trichy
18	VL54	Delay – Sum Beamforming Implementation for Marine Mapping Applications	R.Sathishkumar A.Vimalajuliet	School of Electrical and Electronics, Faculty of Engineering and Technology	SRM University, India